# Reconciling Semantics, Implementation and Users 

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Milner Symposium, Edinburgh, April 16-19, 2012 http:///events.inf.ed.ac.uk/Milner2012h

## My Personal Moto

## You can't protect yourself from good fortune

## On n'est jamais à l'abri d'un coup de bol

## My Priviledged Relation with Robin

- 1972 : Stanford AI Lab
- proving bubble sort with LCF, with Robin as a personal mentor!
- with a TV button on the terminal to watch football ©
- 1976 (?) : reviewing the Full Abstraction paper
- and devoting 7 years of my life to the problem
- 1981? : spending a week in the attic at 'a Crescent
- 1982 : understanding that real-time is $100 \%$ different from Powerdomains, CCS, etc
=> Esterel
- 1983 (?) : understanding the virtues of SCCS
- formalizing Esterel's synchrony principle
- but SCCS was too weak for describing Esterel
- fortunately, SuperGordon's SOS semantics came in!
- 1989 : Robin tells me he wants to come to Sophia for a sabbatical but I am myself going to Paris for a sabbatical ! => Robin occupies my nice office at CMA
- 1989 : the CHAM paper is accepted at POPL but rejected by both referees for TCS
- then accepted by the editor nevertheless
- 1991 : first Milner Lecture in Old College
- 1992 : the Berry-Gonthier Esterel paper in SCP although no referee ever replied!
- 2000 : The Foundations of Esterel In Proof, Language and Interaction: Essays in Honour of Robin Milner
- 2005 : Robin elected foreign associate member of Académie des sciences
- 2010 : Seven Keys to the Digital Futures

Informatics Forum, Edinbrrrrrrrgh Special thanks to Michael Fourman

## Care for Users



## Semantics vs. Implementation vs. users

-Domain equations

- why such a gap between semantics and implementation?
- make semantic as trivial as implementation!
- Synchronous concurrency
- symmetry or asymmetry? SCCS vs. Esterel
- why my users love asymmetric broadcasting
- CHAMonix = Chemical Abstract Machine
- Transition rules are too intellectual, CHAM is for kids !
- Computing vs. proving
- viewing circuits as semantic proof networks
- proving that electrical logic is intrinsically constructive
- the Hurry-Coward correspondence


## Scott Domain Equations

- intTree $=$ int + int $*$ intTree $*$ intTree
- intTree $=$ Leaf of int
| \{ Node of int;
Left of intTree;
Right of intTree \};
- $D=$ int $+(D \rightarrow D)$

What is `=' ? cpo isomorphism instead of equality ! => take fixpoints in cpo categories instead of cpos

## Concrete Data Structures (Kahn-Plotkin)



Beautiful cpo-representation theorem
CDS solve domain equations using only set equality

## Function Spaces => Sequential Algorithms



```
Sor:
\(C \leftarrow C_{1}\) ?
\(t t \rightarrow C_{2}\) ?
    \(t t \rightarrow!t\)
    \(f f \rightarrow!t\)
\(\mathrm{ff} \rightarrow \mathrm{C}_{2}\) ?
\[
t t \rightarrow!t t
\]
\(t t \rightarrow!t\)
\[
f f \rightarrow!f f
\]
ff \(\rightarrow\) !ff
```

Lor:

$$
\begin{aligned}
& C \leftarrow C_{1} \\
& t t \\
& t t \rightarrow t t \\
& f f \\
& C_{2} ? \\
& t t \rightarrow!t t \\
& f f \rightarrow!f f
\end{aligned}
$$

## Several Algorithms per Function




## Sequential Algorithms $\rightarrow$ CDS



- Theorem: algorithms between CDS form a CDS

Corollary: domain equations still solved by equality !

## Parametrized Domain Equations

$$
\begin{aligned}
& \text { ‘a Tree = Leaf of ‘a } \\
& \quad \text { | }\{\text { Node of ‘a; } \\
& \text { Left of intTree; } \\
& \text { Right of intTree }\} ;
\end{aligned}
$$

Semantics (Plotkin-Smyth) : Much fancier category theory

Type checking : more clever (Milner) Implementation: still nothing to do!

Why do you need aspirin + ibuprofen + paracetamol to make the semantics while there is no need for any code?

CDS: because solution is by equality, not isomorphism! => standard pointer manipulation ( $\approx$ naming)

## Science is sooooo conservative

- CDS led me to redefine a $\lambda$-calculus model as a CCC
- Synchronously, 3 papers proposed $\lambda$-models notions
- Hindley-Longo
- Barendregt
- Meyer
- They turned out to be equivalent
- So I lost 3 to 1 !
- But I still think I was right....


## The Darwin Sieve: $p, k p \rightarrow p$



## CSP /CCS = Rendezvous


G. Berry, Milner Symposium, 18/04/2012 18

## Deadlock



Lise and Laure

## Starvation



## Information Propagation by Vibration

Nothing can illustrate vibration better than Bianca Castafiore, Hergé's famous prima donna. See [1] for details. The power of her voice forcibly shakes the microphone and the ears of the poor spectators.
[1] King's Ottokar Sceptre, Hergé, page 29, last drawing.

Sound, light, electrons, program counter, etc.

## SCCS as a model?

- Actions form a free group
$-1, a, b, a b, a^{-1}$
$-a a^{-1}=1$
- abcd ${ }^{-1}$
- Parallel composition is product
- But not sufficient for Esterel!
$-X:=0 ; X:=X+1 ; \leftarrow$ at the same time, in the right order!


## Triggering vs. Preemption

- CCS / SCCS: a.P = start P when a occurs
- Esterel : abort P when a = stop P when a occurs


## Hard and unnatural to specify in SCCS !

- SOS = SuperGordon to the rescue


Newtonian Mechanics = Automatic 0-Delay Attraction Combination


SCCS: free combination Esterel: vector addition


Concurrency + Determinism Calculations are feasible

## Zero Delay and Full Abstraction

Bianca Castafiore singing for the King Muskar XII in Klow, Syldavia. King's Ottokar Sceptre, page 38, first drawing.

Although the speed of sounds is finite, it is fast enough to look infinite. Full abstraction!

## If room is small enough, predictable delay implements zero-delay

Specify with zero-delay
Implement with predictable delay
Control room size


George Heriot's school open day, Oct. $2^{\text {nd }}, 2010$


Edinburgh is also a Synchrony City!

## The ABRO Example

Emit $O$ as soon as $A$ and $B$ have arrived Reset behavior each time $R$ is received


Memory write
R: Request
A : Address
B : Data
O : Write

## Esterel = Linear Specification


loop abort \{ await A || await B \}; emit O;
halt
when $R$
end loop

## The Hierarchical ABRO Circuit



## The Hierarchical ABRO Circuit



## Electicity is Constructive

## Hamlet : $\mathrm{ToBe}=$ ToBe or not ToBe



Electrically stabilizes to for some gate and wire delays but not for all delays

Question: When does a cyclic circuit stabilize for all gate and wire delays?

Theorem: if and only if equations can be solved using only Constructive Boolean Logic (forward propagation of 0's and 1's)
(Mendler, Shiple, and Berry, FMCS April 2012)

